

COPY OF PERS
ORIGINAL FILED



Attorney Docket No. IRV1.PAU.53

Patent Application

#3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pepe, et al.

Application No.: 09/938,686

Filed: August 24, 2001

Examiner: Unknown

Art Unit: 2811

Title: **STACKABLE LAYERS CONTAINING
ENCAPSULATED INTEGRATED CIRCUIT
CHIPS WITH ONE OR MORE OVERLYING
INTERCONNECT LAYERS AND A METHOD
OF MAKING THE SAME**

Irvine, California
October 29, 2001

PETITION UNDER 37 CFR 1.182


Box MISSING PARTS ?

Assistant Commissioner for Patents
Washington, D.C. 20231

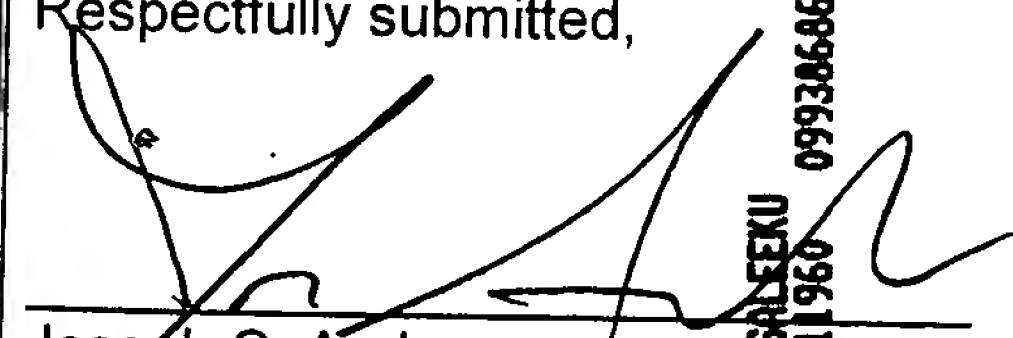
Pursuant to 37 CFR 1.182, and the Notice to File Missing Parts of Nonprovisional Application dated September 28, 2001, Applicants hereby supply the omitted Figures 1a and 1b and petition the United States Patent Office to assign a later filing date of these herein submitted drawings to the overall Application. A declaration referring to the omitted figures is also enclosed.

The \$130 petition fee required under 37 FR 1.17(h) is attached.

The Commissioner of Patents is authorized to charge any amount due, or credit any overpayment, to Deposit Account No. 01-1960. A duplicate copy of this paper is enclosed.

<p>Certificate of Mailing By "Express Mail"</p> <p>Express Mail Label Number: EL 889533022 US</p> <p>Date of Deposit: <u>October 30, 2001</u></p> <p>I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Box Missing Parts, Assistant Commissioner for Patents, Washington, DC 20231</p> <p>By Angela Williams</p> <p></p> <p>Signature October 30, 2001</p>

Respectfully submitted,


Joseph C. Andras,
Registration No. 33,469
MYERS DAWES & ANDRAS
19900 MacArthur Blvd., Suite 1300
Irvine, CA 92612
(949) 223-9600

Adjustment Date: 01/07/2002
12/18/2001 AGOITOM 00000010 011960 09938686
01 FC:122 130.00 CR

2/18/2001 AGOITOM 00000010 011960 09938686
01 FC:122 130.00 CR